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PAGE 1/11 * RCVD AT 5/26/2006 2:11:39 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-1/5 * DNIS:2738300 * CSID: * DURATION (mm-ss):02:48

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Fried *et al.*

Examiner: Wilson, Scott R.

Serial No.: 10/780,393

Art Unit: 2826

Filed: 02/17/2004

Dkt. No.: BUR920010162US2

Title: **DUAL DOUBLE GATE TRANSISTOR AND METHOD FOR FORMING**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR RECONSIDERATION

Sir:

This Request for Reconsideration is in response to the Office Action mailed March 2,
2006.

10/780,393

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